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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RE APPLICATION OF:
Kimihiro MATSUSE, et al.

: GROUP: 2814

SERIAL NUMBER: 09/530,588

: ATTENTION:
Application Division
Customer Corrections

FILED: MAY 05, 2000

FOR: METHOD OF FORMING SEMICONDUCTOR WIRING STRUCTURES

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

The Patent Office is requested to provide a corrected Official Filing Receipt for the attached. No fees are required. If you have any questions, please do not hesitate to contact us.

Respectfully submitted,

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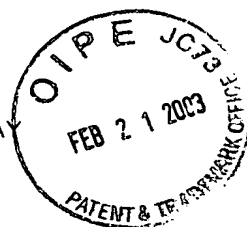


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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/530.588	05/05/2000	2814	2370	2312-0866- 2P	4	8	4

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CONFIRMATION NO. 6686
CORRECTED FILING RECEIPT
OC000000009101113

Date Mailed: 11/14/2002

Receipt is acknowledged of a CPA in this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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11-18-02
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Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP98/04983 11/05/1998

Foreign Applications

JAPAN 9-319059 11/05/1997
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If Required, Foreign Filing License Granted 06/06/2000

CPA filed on: 11/04/2002

Projected Publication Date: -> 02/20/2003

Non-Publication Request: No

Early Publication Request: No

Title

WIRING STRUCTURE OF SEMICONDUCTOR DEVICE, ELECTRODE, AND METHOD FOR FORMING THEM

Preliminary Class

257

PLEASE NOTE THAT THE SECOND APPLICANT'S CITY IS INCORRECT. IT SHOULD READ AS FOLLOWS:

2ND= HAYASHI OTSUKI, NAKAKOMA-GUN, JAPAN

PLEASE NOTE THAT THE TITLE IS INCORRECT. IT SHOULD READ AS FOLLOWS:

METHOD OF FORMING SEMICONDUCTOR WIRING STRUCTURES